

Applicant : Michael B. Propp et al.
Serial No. : 09/837,760
Filed : April 17, 2001
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Attorney's Docket No.: 09539-011001

Amendments to the Drawings:

The attached replacement sheet of drawings includes changes to Fig. 2 and Fig. 3 and replaces the original sheet including Fig. 2 and Fig. 3.

In Figs. 2 and 3 the inverters have been labeled, and in Fig. 2 the label "B" has been replaced with "BUFFER".

Attachments following last page of this Amendment:

Replacement Sheet (1 page)
Annotated Sheet Showing Change(s) (1 page)

REMARKS

Claims 1-3 were examined. Applicant has cancelled claims 1-3 and added new claims 4-23. Accordingly, Applicant requests examination of new claims 4-23.

The Office Action objects to figures 2 and 3 for improper labeling. Applicant has amended the drawings accordingly.

The Office Action (i) further objects to the drawings for failure to show every feature of the claims, (ii) objects to claims 2 and 3 for informalities, (iii) rejects claims 1-3 under 35 U.S.C. § 112, first paragraph, and (iv) rejects claims 2-3 under 35 U.S.C. § 112, second paragraph. Applicant submits that these objections and rejections are moot in light of claim changes and drawing changes proposed by this amendment.

Claim 1 also stands rejected under 35 U.S.C. § 102(b) as being anticipated by Eitan (5,796,313). Applicant submits that this rejection is overcome by claim changes proposed by this amendment. Applicant's new claims recite a circuit that includes a unique combination and interoperation of two delay devices, a sampling signal line, and a sampling device. Eitan does not disclose or suggest at least the recited "sampling signal line coupled to, and configured to provide a sampling signal based on, an output of [a] first delay device; and a sampling device coupled to an output of [a] second delay device and coupled to the sampling signal line, the sampling device configured to sample the output of the second delay device based on a value of the sampling signal" (claim 4, emphasis added; see also claim 22). In fact, Eitan is not directed toward sampling applications and does not describe the recited sampling signal line and sampling device. Rather, Eitan describes an oscillator designed to produce a low-power output signal having a frequency that is independent of variations in supply voltage (Abstract).

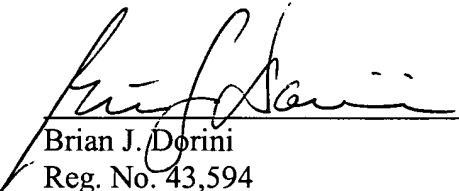
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Enclosed is a \$1020.00 check for the Petition for Extension of Time fee. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: JANUARY 3, 2005 (MONDAY)



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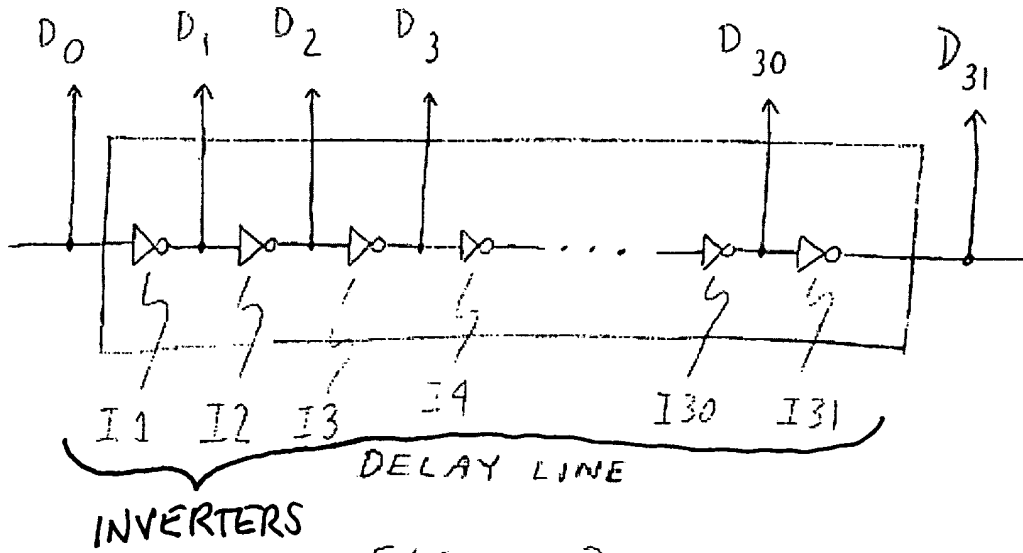
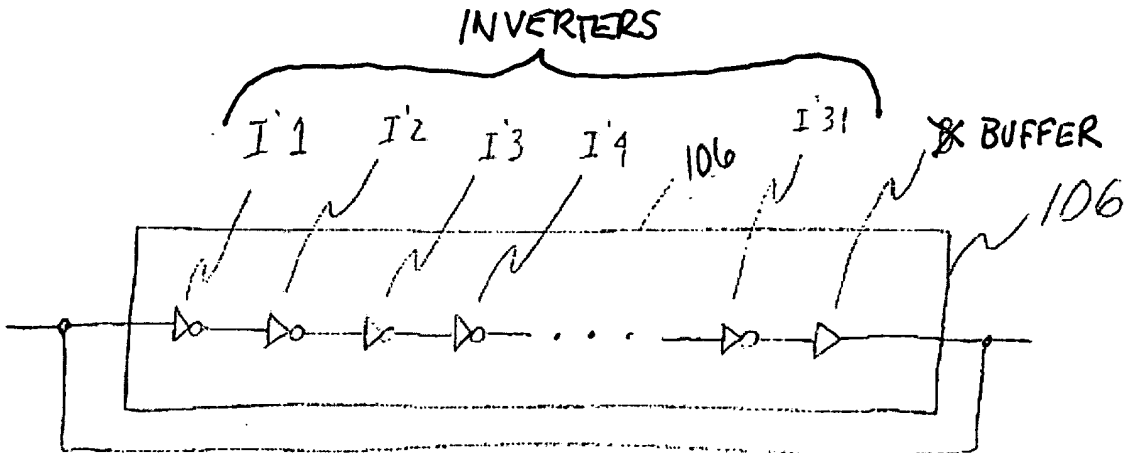


FIGURE 2



DELAY LINE OSCILLATOR

FIGURE 3